

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L8    | 25   | (memory with (integrated adj circuit) with testing) and ((boundary adj scan) with (integrated adj circuit)) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:44 |
| L9    | 358  | (714/727.ccls.)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:45 |
| L10   | 158  | (714/727.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:50 |
| L11   | 135  | (714/726.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:50 |
| L12   | 113  | (714/724.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:51 |
| L13   | 92   | (714/733.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:52 |
| L14   | 24   | (716/18.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:53 |
| L15   | 105  | (714/30.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:54 |
| L16   | 26   | (714/731.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2004/11/22 17:55 |

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|-----|----|---|---|----|-----|------------------|
| L17 | 20 | (324/158.1.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/22 17:55 |
| L18 | 23 | (324/73.1.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/22 17:57 |
| L19 | 4  | (712/220.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/22 17:58 |
| L21 | 21 | (714/718.ccls.) and (IC or (integrated adj circuit)) and memory and (boundary adj scan)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/11/22 17:58 |